

## WEST Search History

DATE: Thursday, November 09, 2006

Hide?	<u>Set</u> <u>Name</u>	<u>Query</u>	<u>Hit</u> <u>Count</u>
	<i>DB=PGPB,USPT; PLUR=NO; OP=ADJ</i>		
<input type="checkbox"/>	L92	l73 and advanced.as.	35
<input type="checkbox"/>	L91	(advanced micro).as. and (fast with slow with instruction\$1)	1
<input type="checkbox"/>	L90	treat.xp. and l88	0
<input type="checkbox"/>	L89	(advanced micro).as. and (fast with slow)	132
<input type="checkbox"/>	L88	(advanced micro).as. and (fast same slow)	181
<input type="checkbox"/>	L87	l39 same fast same slow	0
<input type="checkbox"/>	L86	l39 same scalar same floating	0
<input type="checkbox"/>	L85	l39 same integer same floating	1
<input type="checkbox"/>	L84	L73 with scalar with floating	0
<input type="checkbox"/>	L83	L73 with integer with floating	0
<input type="checkbox"/>	L82	L73 with busy	0
<input type="checkbox"/>	L81	L73 with unavailable	0
<input type="checkbox"/>	L80	L73 with available	4
<input type="checkbox"/>	L79	"embodiment"	2
<input type="checkbox"/>	L78	"alternative embodiment"	0
<input type="checkbox"/>	L77	(micro-instruction\$1 or micro-code\$1 or micro-routine\$1 or micro-program\$1) with "embodiment"	0
<input type="checkbox"/>	L76	(micro-instruction\$1 or micro-code\$1 or micro-routine\$1 or micro-program\$1) with "alternative embodiment"	0
<input type="checkbox"/>	L75	(micro-instruction\$1 or micro-code\$1 or micro-routine\$1 or micro-program\$1) with alternative embodiment\$1	0
<input type="checkbox"/>	L74	(micro-instruction\$1 or micro-code\$1 or micro-routine\$1 or micro-program\$1) with alternative	15
<input type="checkbox"/>	L73	(microinstruction\$1 or microcode\$1 or microroutine\$1 or microprogram\$1) with alternative	358
<input type="checkbox"/>	L72	L39 and L69	3
<input type="checkbox"/>	L71	L48 and L69	3
<input type="checkbox"/>	L70	L27 and L69	5
<input type="checkbox"/>	L69	(712/209).ccls.	344
<input type="checkbox"/>	L68	L61 same arm instruction\$1	55
<input type="checkbox"/>	L67	6654875.pn.	1
<input type="checkbox"/>	L66	6654875.uref.	1

<input type="checkbox"/>	L65	L62 and matsushita.as.	7
<input type="checkbox"/>	L64	6834339.uref.	0
<input type="checkbox"/>	L63	6834339.pn.	1
<input type="checkbox"/>	L62	shuji.in. and kondo.in.	52
<input type="checkbox"/>	L61	thumb instruction\$1	81
<input type="checkbox"/>	L60	(microinstruction\$1 or microcode or microroutine or microprogram) adj substitution	5
<input type="checkbox"/>	L59	L39 and L58	0
<input type="checkbox"/>	L58	L56 or L57	109
<input type="checkbox"/>	L57	substitut\$3 with (micro-instruction\$1 or micro-code or micro-routine or micro-program)	5
<input type="checkbox"/>	L56	substitut\$3 with (microinstruction\$1 or microcode or microroutine or microprogram)	104
<input type="checkbox"/>	L55	fast execution same slow execution	9
<input type="checkbox"/>	L54	3760369.uref.	23
<input type="checkbox"/>	L53	alternative microinstructions	3
		<i>DB=USPT,PGPB; PLUR=NO; OP=ADJ</i>	
<input type="checkbox"/>	L52	('4691278' '4736289' '5123096')![[pn]	3
		<i>DB=PGPB,USPT; PLUR=NO; OP=ADJ</i>	
<input type="checkbox"/>	L51	alternative microcode	17
<input type="checkbox"/>	L50	L11.ti,ab,clm.	0
<input type="checkbox"/>	L49	L39 and L48	0
<input type="checkbox"/>	L48	instruction\$1 with synonym\$1	121
<input type="checkbox"/>	L47	L44 and L46	9
<input type="checkbox"/>	L46	integer implementation	115
<input type="checkbox"/>	L45	L43 and L44	0
<input type="checkbox"/>	L44	floating point implementation	116
<input type="checkbox"/>	L43	scalar implementation	19
<input type="checkbox"/>	L42	4419726.uref.	5
<input type="checkbox"/>	L41	power with L39	5
<input type="checkbox"/>	L40	state with L39	6
<input type="checkbox"/>	L39	alternative instructions	292
<input type="checkbox"/>	L38	instruction alternatives	2
<input type="checkbox"/>	L37	L30 same arm instruction\$1	55
<input type="checkbox"/>	L36	6654875.pn.	1
<input type="checkbox"/>	L35	6654875.uref.	1
<input type="checkbox"/>	L34	L31 and matsushita.as.	7
<input type="checkbox"/>	L33	6834339.uref.	0
<input type="checkbox"/>	L32	6834339.pn.	1

<input type="checkbox"/>	L31	shuji.in. and kondo.in.	52
<input type="checkbox"/>	L30	thumb instruction\$1	81
<input type="checkbox"/>	L29	(microinstruction\$1 or microcode or microroutine or microprogram) adj substitution	5
<input type="checkbox"/>	L28	L8 and L27	0
<input type="checkbox"/>	L27	L25 or L26	109
<input type="checkbox"/>	L26	substitut\$3 with (micro-instruction\$1 or micro-code or micro-routine or micro-program)	5
<input type="checkbox"/>	L25	substitut\$3 with (microinstruction\$1 or microcode or microroutine or microprogram)	104
<input type="checkbox"/>	L24	fast execution same slow execution	9
<input type="checkbox"/>	L23	3760369.uref.	23
<input type="checkbox"/>	L22	alternative microinstructions	3
		<i>DB=USPT,PGPB; PLUR=NO; OP=ADJ</i>	
<input type="checkbox"/>	L21	('4691278'  '4736289'  '5123096')![pn]	3
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<input type="checkbox"/>	L20	alternative microcode	17
<input type="checkbox"/>	L19	L11.ti,ab,clm.	0
<input type="checkbox"/>	L18	L8 and L17	0
<input type="checkbox"/>	L17	instruction\$1 with synonym\$1	121
<input type="checkbox"/>	L16	L13 and L15	9
<input type="checkbox"/>	L15	integer implementation	115
<input type="checkbox"/>	L14	L12 and L13	0
<input type="checkbox"/>	L13	floating point implementation	116
<input type="checkbox"/>	L12	scalar implementation	19
<input type="checkbox"/>	L11	4419726.uref.	5
<input type="checkbox"/>	L10	power with L8	5
<input type="checkbox"/>	L9	state with L8	6
<input type="checkbox"/>	L8	alternative instructions	292
<input type="checkbox"/>	L7	instruction alternatives	2
<input type="checkbox"/>	L6	two different microprograms	4
<input type="checkbox"/>	L5	two different microinstruction routines	0
<input type="checkbox"/>	L4	two different micro-instruction routines	0
<input type="checkbox"/>	L3	two different micro-code routines	0
<input type="checkbox"/>	L2	two different microcode routines	0
<input type="checkbox"/>	L1	floating point routine same integer routine	1

END OF SEARCH HISTORY